

The download cable XB-PCIII 2.01 Parallel Cable III is connected to LPT port of a personal computer and allows:



- to carry out configuration a family of FPGA Xilinx, making in accordance with the technology of SRAM: Virtex-II, Virtex E, Virtex, Spartan-6, Spartan-3E, Spartan-3.
- to provide with programming a family of CPLD Xilinx, making in accordance with the technology of EEPROM: CoolRunner-II, CoolRunner XPLA3, XC9500XL, XC9500.
- to produce programming of configurative PROM: XCF01, XCF02, XCF04, XCF08, XCF16 and XCF32.

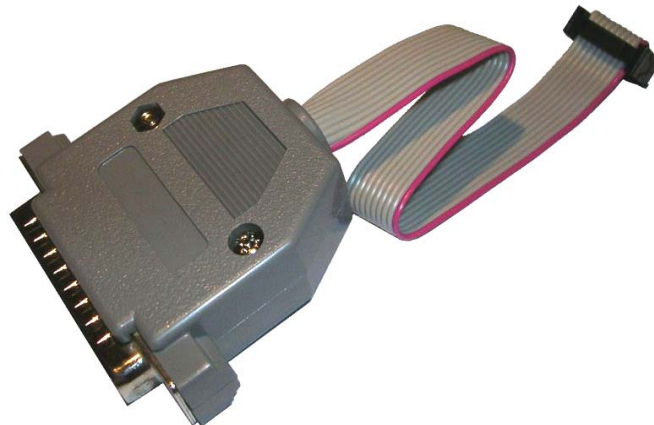


Fig. 1. General view of the download cable XB-PCIII 2.01 Parallel Cable III

The download cable has following characteristic features:

- allows to program chips powered by either from 5.0 V or 3.3 V B;
- provides a quick and cheap way of in-system programming;
- supports data downloading directly from the design environment Xilinx ISE WebPack;
- is compatible with the standard 25-contact parallel port (LPT) of a personal computer;
- has a standard 10-contact connector for connection to the board.

Correspondence of cable interface signals and parallel port contacts is shown in the table 1. The scheme of the download cable XB-PCIII 2.01 is shown in the figure 2.

Table 1

Correspondence of cable interface signals and parallel port contacts.

Contact	JTAG
2	TDI
3	TCK
4	TMS
13	TDO
15	VCC
18-25	GND

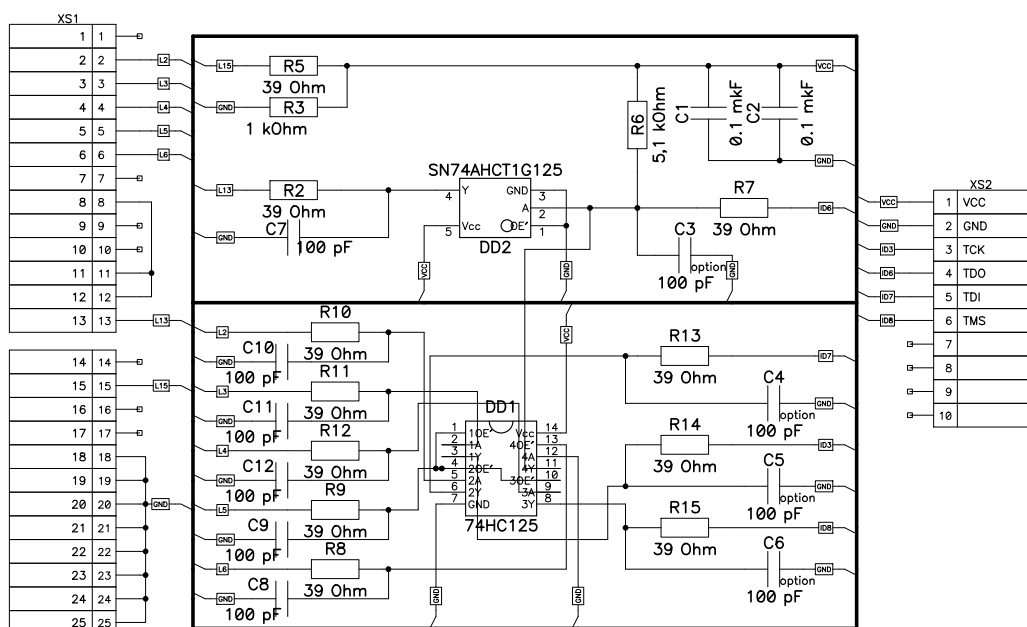


Fig. 2. Scheme XB-PCIII 2.01 Parallel Cable III

10-contact connector is shown in the figure 3, the purpose of its contacts is shown in the table 2.

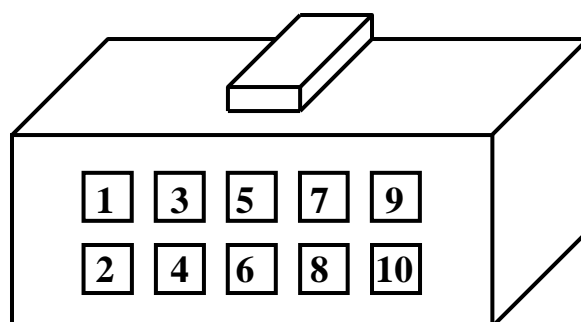


Fig. 3. The view of 10-contact connector XB-PCIII 2.01 Parallel Cable III

Outputs designation of the 10-contact connector of
XB-PCIII 2.01 Parallel Cable III

Contact	JTAG-mode	
	Designation	Description
1	VCC	Supply voltage
2	GND	"Earth"
3	TCK	Clock signal
4	TDO	Data output
5	TDI	Data input
6	TMS	Control of the final automatic machine JTAG
7-10	-	Does not use

Power XB-PCIII 2.01 Parallel Cable III is carried out by a board on which a programmable device is installed. During the use of development boards or sockets for programming, it is imperative to set power blocking capacitors. The absence of such capacitors may cause problems in programming and even damage the chip.

Supply voltage range:

- 4.5 - 5.5 for devices with VCC=5 V;
- 2.5 - 3.6 for devices with VCC=3 V.

Packaging arrangements:

- The download cable XB-PCIII 2.01 Parallel Cable III;
- Description of the download cable;
- Examples of projects for Xilinx ISE WebPack;
- Description of the kind of FPGA Xilinx.